



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Isamu Kobori, et al. Art Unit : Unknown
Serial No. : 10/623,581 Examiner : Unknown
Filed : July 22, 2003 Confirmation No.: Unknown
Title : METHOD OF MANUFACTURING A SEMICONDUCTOR METHOD OF
MANUFACTURING A THIN-FILM TRANSISTOR AND THIN-FILM
TRANSISTOR

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Under 35 USC §120, this application relies on the earlier filing date of application serial number 09/016,999, filed on February 2, 1998. The attached list of references were submitted to the Office in the prior application and, therefore, are not provided in this application.

Please apply any charges or credits to Deposit Account No. 06-1050.

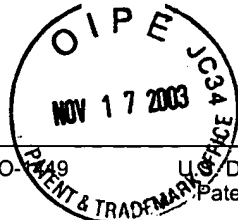
Respectfully submitted,

Date: 11/17/03



John F. Hayden
Reg. No. 37,640

Fish & Richardson P.C.
1425 K Street, N.W.
11th Floor
Washington, DC 20005-3500
Telephone: (202) 783-5070
Facsimile: (202) 783-2331



Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 07977-024003	Application No. 10/623,581
	Applicant Isamu Kobori et al.		
	Filing Date July 22, 2003		Group Art Unit

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AC	2-150017	06/08/90	Japan			ABS	
	AD	2-224255	09/06/90	Japan			ABS	
	AE	6-84793	03/25/94	Japan			ABS	
	AF	6-124962	05/06/94	Japan			ABS	
	AG							

Other Documents (include Author, Title, Date, and Place of Publication)		
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EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	